MIC5271

µCap Negative Low Dropout Regulator

Features

- Stable with Ceramic or Tantalum Capacitors
- Standard Fixed Output Voltage Options: 3.0V and 5.0V
- Adjustable Output Voltage Option: (–1.2V to –14V)
- Positive and Negative Enable Thresholds
- Low Dropout Voltage: –500 mV @ –100 mA
- Low Ground Current: –25 µA @ Load = –100 µA
- Tight Initial Accuracy: ±2%
- Tight Load and Line Regulation
- Thermal Shutdown and Current-Limit Protection
- IttyBitty 5-Pin SOT23 Packaging
- Zero-Current Off Mode

Applications

- GaAsFET Bias
- Portable Cameras and Video Recorders
- PDAs
- Battery-Powered Equipment
- Post-Regulation of DC/DC Converters

General Description

The MIC5271 is a µCap 100 mA negative regulator in a SOT23-5 package. With better than 2% initial accuracy, this regulator provides a very accurate supply voltage for applications that require a negative rail. The MIC5271 sinks 100 mA of output current at very low dropout voltage (500 mV typical, 700 mV maximum at 100 mA of output current).

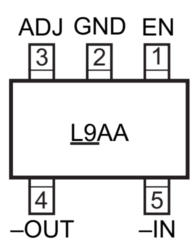
The µCap regulator design is optimized to work with low-value, low-cost ceramic capacitors. The output typically requires only a 1 µF capacitance for stability.

Designed for applications where small packaging and efficiency are critical, the MIC5271 combines LDO design expertise with IttyBitty packaging to improve performance and reduce power dissipation. Ground current is optimized to help improve battery life in portable applications. The MIC5271 also includes a TTL-compatible enable pin, allowing the user to put the part into a zero-current off mode, in which the ground current is only ±1 µA, typical.

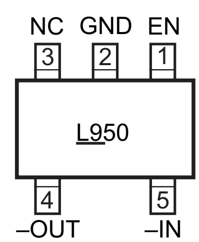
The MIC5271 is available in the 5-pin SOT23 package for space saving applications and it is available with an adjustable output.

Package Types

MIC5271YM5
5-Lead SOT23 (M5)
Adjustable Output Voltage
(Top View)



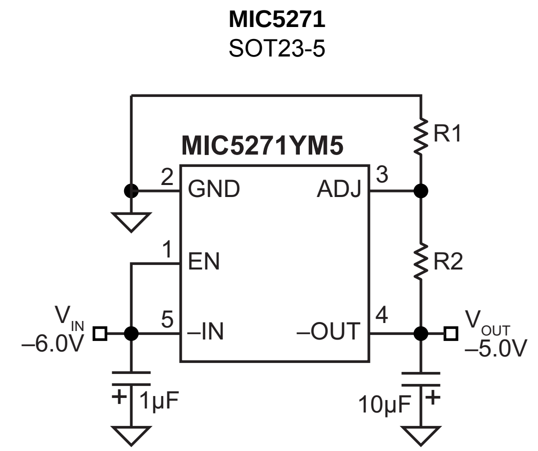
MIC5271-5.0YM5 *
5-Lead SOT23 (M5)
Fixed Output Voltage
(Top View)



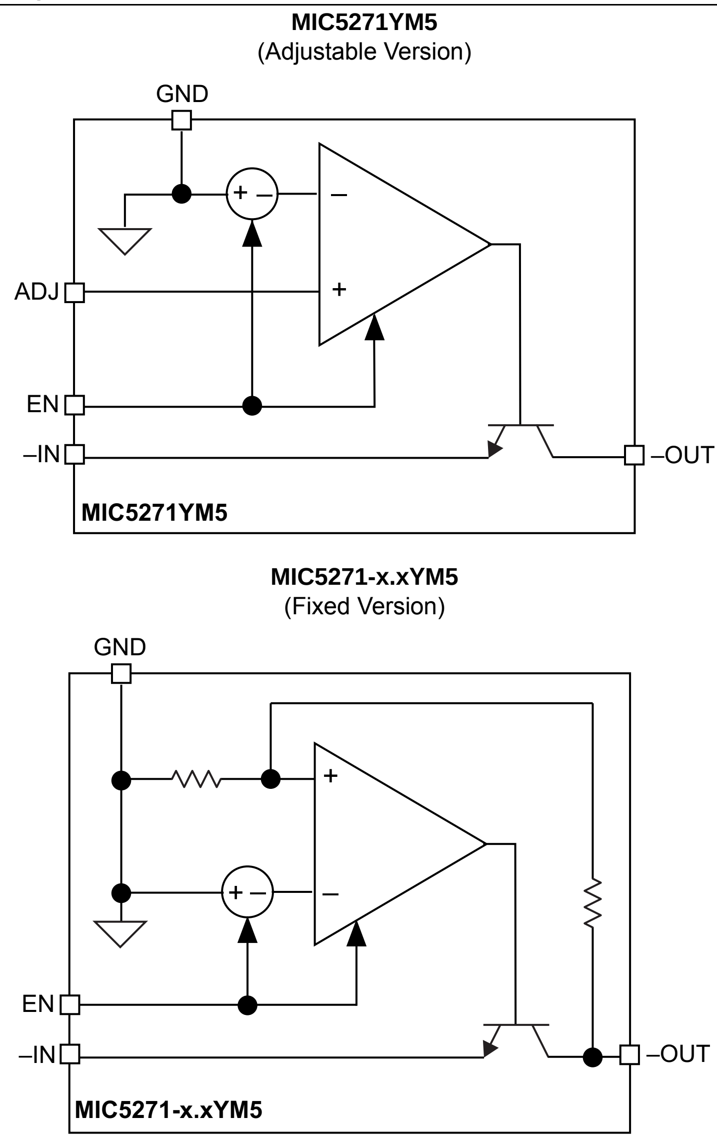
* 5.0V pinout shown. 3.0V version is identical.

Please see pin descriptions in Table 3-1.

Typical Application Circuit



Functional Block Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Input Voltage ($V_{–IN}$) .... –20V to +0.3V
Enable Voltage (V_{EN}) .... –20V to +20V
Power Dissipation .... Internally Limited
ESD Rating .... Note 1

Operating Ratings ‡

Input Voltage ($V_{–IN}$) .... –16V to –3.3V
Enable Voltage (V_{EN}) .... –16V to +16V

† **Notice:** Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.

‡ **Notice:** The device is not guaranteed to function outside its operating ratings.

Note 1: Devices are ESD sensitive. Handling precautions recommended.

TABLE 1-1: ELECTRICAL CHARACTERISTICS

Electrical Characteristics: $V–_{IN}$ = $V–_{OUT}$ – 1.0V; C_{OUT} = 4.7 µF, I_{OUT} = 100 µA; T_J = +25°C, **bold** values indicate –40°C ≤ T_J ≤ +125°C; unless otherwise noted. Note 1

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Output Voltage Accuracy	$V–_{OUT}$	–2	—	2	%	Variation from nominal $V–_{OUT}$.
		–3	—	**3**		
Output Voltage Temperature Coefficient	$\Delta V–_{OUT}/\Delta T$	—	100	—	ppm/°C	Note 2
Line Regulation	$\Delta V–_{OUT}$/ $V–_{OUT}$	—	0.04	0.15	%/V	$V–_{IN}$ = $V–_{OUT}$ – 1V to –16V
				0.2		
Load Regulation	$\Delta V–_{OUT}$/ $V–_{OUT}$	—	0.4	1.8	%	I_{OUT} = –100 µA to –100 mA, Note 3
				2.0		
Dropout Voltage, Note 4	$V–_{IN}$ – $V–_{OUT}$	—	–55	—	mV	I_{OUT} = –100 µA
		—	–360	–500		I_{OUT} = –50 mA
		—	–500	–700		I_{OUT} = –100 mA
				–900		
Ground Current, Note 5	I_{GND}	—	–25	**–100**	µA	I_{OUT} = –100 µA
		—	–0.9	—	mA	I_{OUT} = –50 mA
		—	–2.0	–3.0		I_{OUT} = –100 mA
Ground Current in Shutdown	I_{GND_SD}	–1.0	0.1	1.0	µA	V_{EN} = ±0.6V
Ripple Rejection	PSRR	—	50	—	dB	f = 120 Hz
Current Limit	I_{LIMIT}	—	235	**350**	mA	$V–_{OUT}$ = 0V
Turn-On Time	t_{ON}	—	60	—	µs	Time to V_{OUT} = 90% (nominal)
Input Low Voltage	V_{EN}	—	—	±0.6	V	Regulator OFF
Input High Voltage		±2.0	—	—		Regulator ON
Enable Input Current	I_{EN}	—	—	**0.1**	µA	V_{EN} = ±0.6V and –2.0V
		—	5.6	**10.0**		V_{EN} = +2.0V

Note 1: Specification for packaged product only

2: Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.

3: Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 100 µA to 100 mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.

4: Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential.

5: Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.

TEMPERATURE SPECIFICATIONS (Note 1)

Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Temperature Ranges						
Junction Temperature Range	T_J	–40	—	+125	°C	—
Storage Temperature Range	T_S	–65	—	+150	°C	—
Lead Temperature	—	—	—	+260	°C	Soldering, 10s
Package Thermal Resistances						
Thermal Resistance SOT23-5	θ_{JA}	—	235	—	°C/W	—

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e., T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum +125°C rating. Sustained junction temperatures above +125°C can impact the device reliability.

2: The maximum allowable power dissipation is a function of the maximum junction temperature, $T_{J(MAX)}$ the junction-to-ambient thermal resistance, θ_{JA}, and the ambient temperature, T_A. The maximum allowable power dissipation at any ambient temperature is calculated using: $P_{D(MAX)} = (T_{J(MAX)} - T_A) \div \theta_{JA}$, where θ_{JA} is 235°C/W. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown. See the “Thermal Considerations” sub-section in the Application Information for details.

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

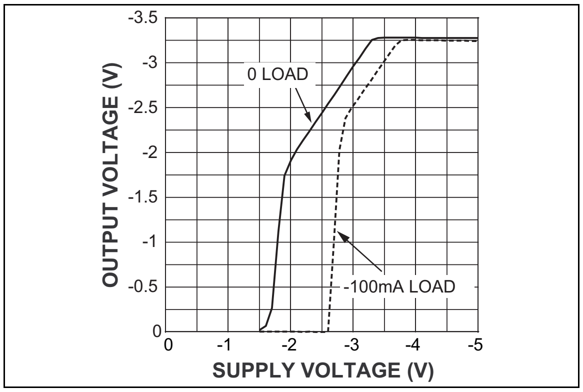


FIGURE 2-1: *Dropout Characteristics.*

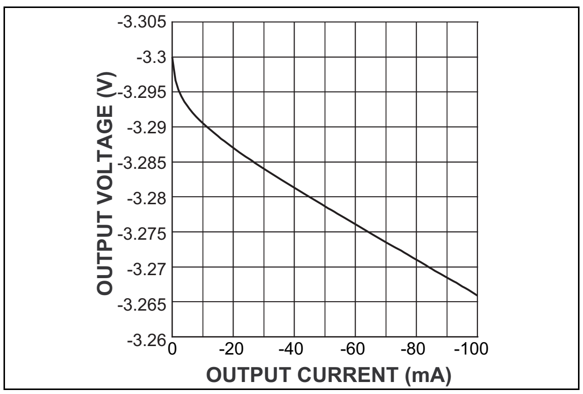


FIGURE 2-2: *Output Voltage vs. Output Current.*

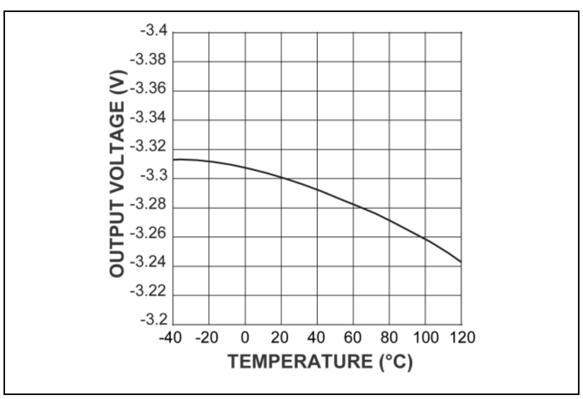


FIGURE 2-3: *Output Voltage vs. Temperature.*

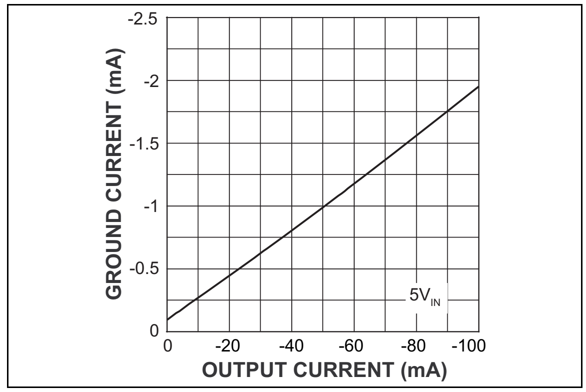


FIGURE 2-4: *Ground Current vs. Output Current.*

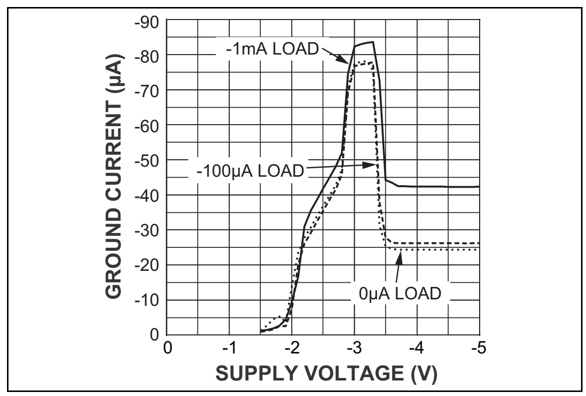


FIGURE 2-5: *Ground Current vs. Input Voltage.*

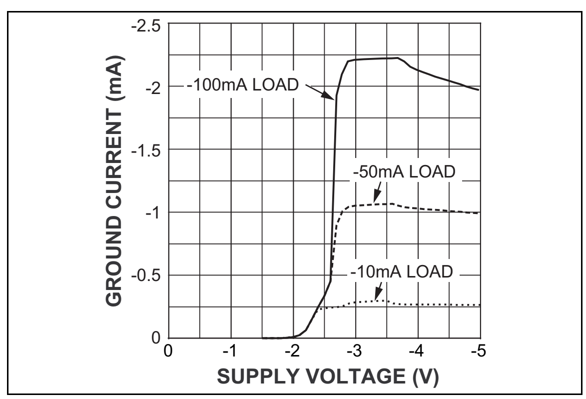


FIGURE 2-6: *Ground Current vs. Input Voltage.*

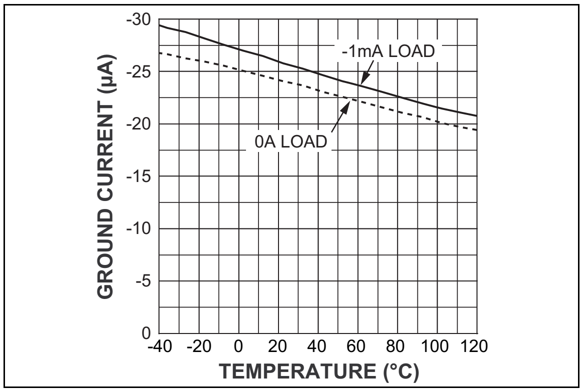


FIGURE 2-7: *Ground Current vs. Temperature.*

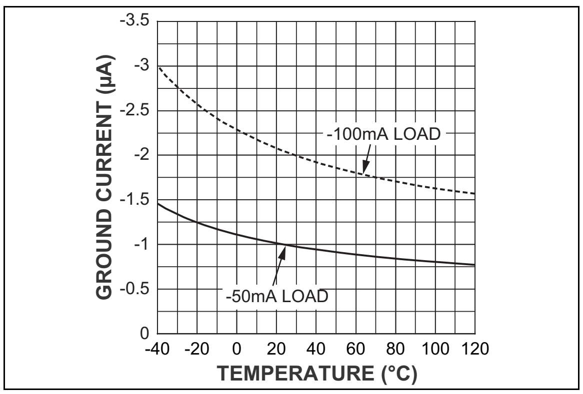


FIGURE 2-8: *Ground Current vs. Temperature.*

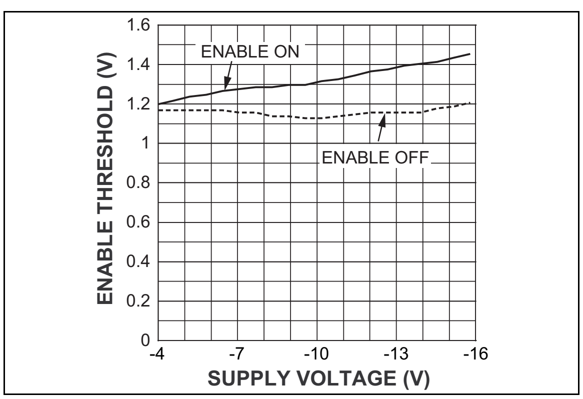


FIGURE 2-9: *Positive Enable Threshold vs. Supply Voltage.*

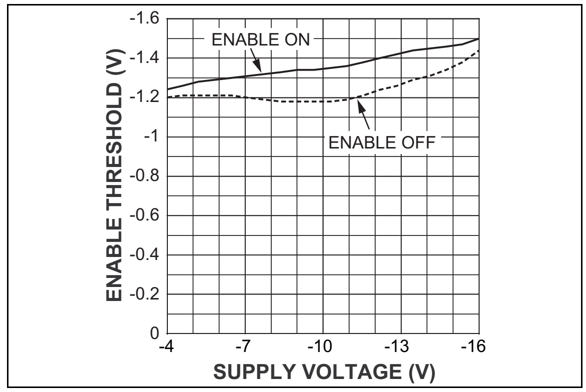


FIGURE 2-10: *Negative Enable Threshold vs. Supply Voltage.*

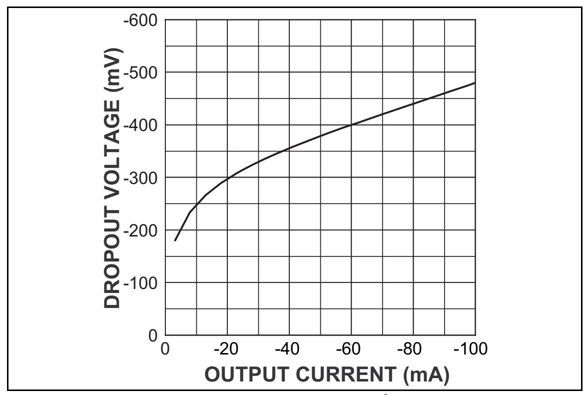


FIGURE 2-11: *Dropout Voltage vs. Output Current.*

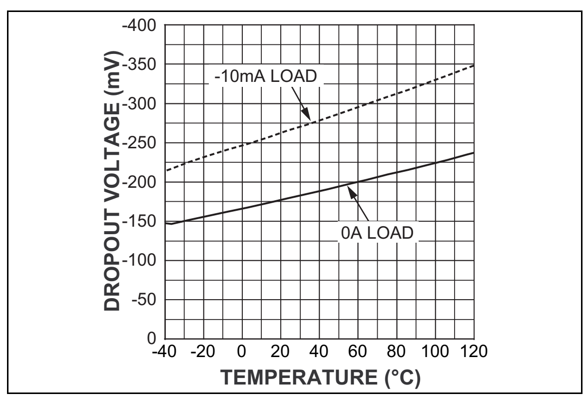


FIGURE 2-12: *Dropout Voltage vs. Temperature.*

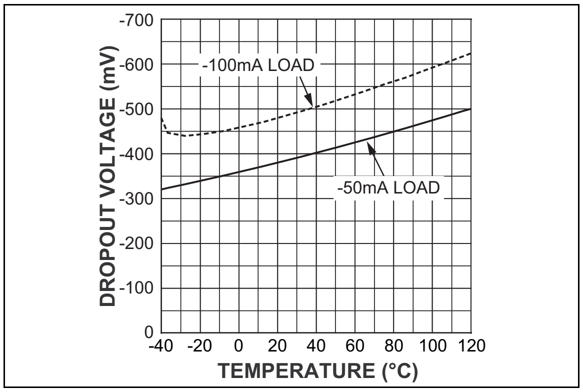


FIGURE 2-13: *Dropout Voltage vs. Temperature.*

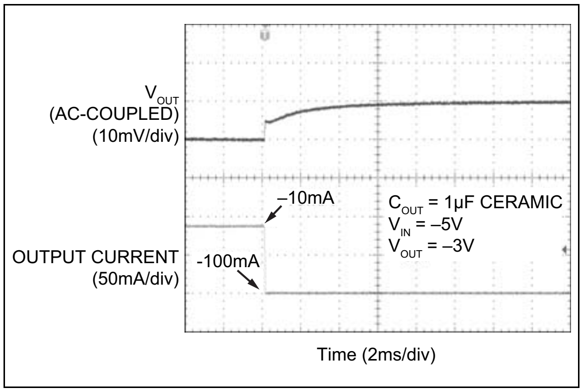


FIGURE 2-14: *Load Transient.*

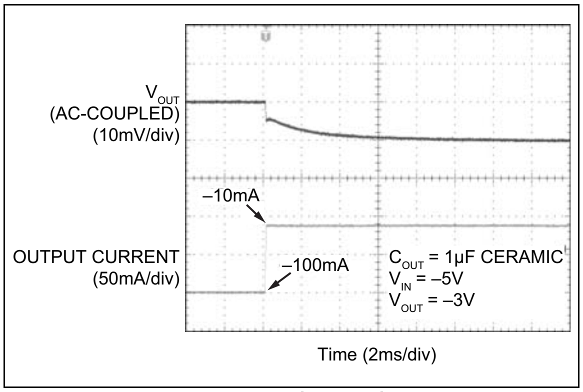


FIGURE 2-15: *Load Transient.*

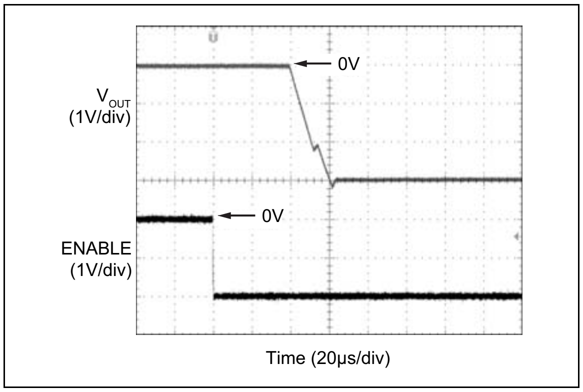


FIGURE 2-16: *Negative Enable Transient.*

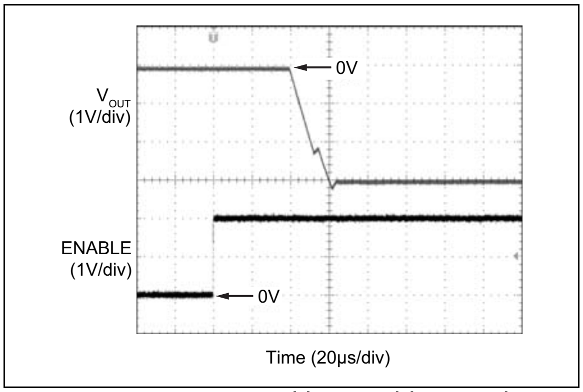


FIGURE 2-17: *Positive Enable Transient.*

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

Pin Number Adjustable	Pin Number Fixed	Pin Name	Description
1	1	EN	Enable Input. TTL logic-compatible enable input. Logic HIGH = ON, Logic LOW or open = OFF.
2	2	GND	Ground.
3	—	ADJ	Adjustable (Input): Adjustable feedback output connects to resistor voltage divider.
—	3	NC	No Connect. Leave unconnected.
4	4	–OUT	Negative Regulator Output.
5	5	–IN	Negative Supply Input.

4.0 APPLICATION INFORMATION

The MIC5271 is a general-purpose negative voltage regulator that can be used in a system that requires a clean negative voltage. This includes the post regulation of DC/DC converters (transformer or charge pump based voltage converters). These negative voltages typically require a negative low dropout voltage regulator to provide a clean output from noisy input power.

4.1 Input Capacitor

A 1 µF input capacitor should be placed from –IN to GND if there is more than two inches of wire or trace between the input and the AC filter capacitor or if a battery is used as the input.

4.2 Output Capacitor

The MIC5271 requires an output capacitor for stable operation. A minimum of 1 µF of output capacitance is required. The output capacitor can be increased without limitation to improve transient response. The output does not require ESR to maintain stability; therefore a ceramic capacitor can be used. High-ESR capacitors may cause instability. Capacitors with an ESR of 3Ω or greater at 100 kHz can cause a high-frequency oscillation.

Low-ESR tantalums are recommended due to the tight capacitance tolerance over temperature. The Z5U dielectric can change capacitance value by as much 50% over temperature, and the Y5V dielectric can change capacitance value by as much as 60% over temperature. To use a ceramic chip capacitor with the Y5V dielectric, the value must be much higher than a tantalum to ensure the same minimum capacitor value over temperature.

4.3 No-Load Stability

The MIC5271 does not require a load for stability.

4.4 Enable Input

The MIC5271 comes with an enable pin that allows the regulator to be disabled. Forcing the enable pin higher than the negative threshold and lower than the positive threshold disables the regulator and sends it into a “zero” off-mode current state. In this state, current consumed by the regulator goes nearly to zero, typically drawing only ±1 µA. The MIC5271 will be in the “on” mode when the voltage applied to the enable pin is either greater than the positive threshold or less than the negative threshold.

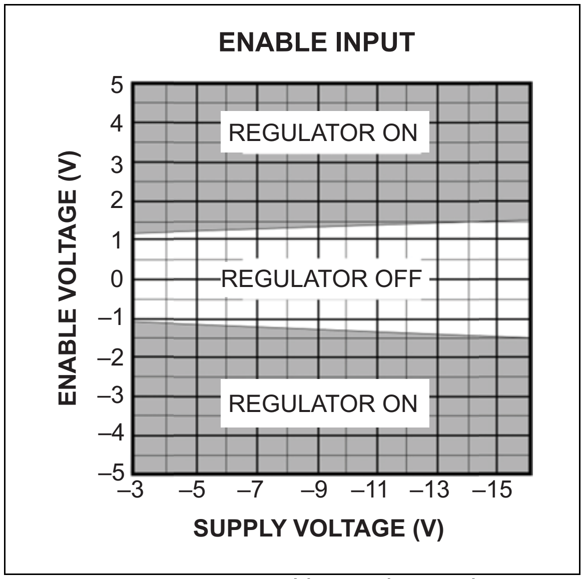


FIGURE 4-1: Positive and Negative Enable Voltage vs. Supply Voltage.

4.5 Thermal Considerations

Absolute values will be used for thermal calculations to clarify the meaning of power dissipation and voltage drops across the part.

Proper thermal design for the MIC5271-5.0YM5 can be accomplished with some basic design criteria and some simple equations. The following information must be known to implement your regulator design:

- V_{IN} = Input voltage
- V_{OUT} = Output voltage
- I_{OUT} = Output current
- T_A = Ambient operating temperature
- I_{GND} = Ground current

Maximum power dissipation can be determined by knowing the ambient temperature (T_A), the maximum junction temperature (+125°C), and the thermal resistance (junction-to-ambient). The thermal resistance for this part, assuming a minimum footprint board layout, is +235°C/W. The maximum power dissipation at an ambient temperature of +25°C can be determined with Equation 4-1 and Equation 4-2:

EQUATION 4-1:

$$P_{D(MAX)} = \frac{T_{J(MAX)} - T_A}{\theta_{JA}}$$

EQUATION 4-2:

$$P_{D(MAX)} = \frac{125°C - 25°C}{235°C/W} = 425mW$$

The actual power dissipation of the regulator circuit can be determined using Equation 3:

EQUATION 4-3:

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} + (V_{IN} \times I_{GND})$$

Substituting $P_{D(MAX)}$, determined above, for P_D and solving for the operating conditions that are critical to the application will give the maximum operating conditions for the regulator circuit. The maximum power dissipation number cannot be exceeded for proper operation of the device. The maximum input voltage can be determined using the output voltage of 5.0V and an output current of 100 mA. Ground current, of 2 mA for 100 mA of output current, can be taken from Table 1-1.

- 425 mW = (V_{IN} – 5.0V) x 100 mA + V_{IN} x 2 mA
- 425 mW = (100 mA x V_{IN} + 2 mA x V_{IN}) – 500mW
- 925 mW = 102 mA x V_{IN}
- V_{IN} = 9.07V (maximum)

Therefore, a –5.0V application at –100 mA of output current can accept a maximum input voltage of –9.07V in a SOT-23 package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to “Regulator Thermals” section of Microchip’s Designing with Low Dropout Voltage Regulators handbook and AN792, A Method to Determine How Much Power an SOT23 Can Dissipate in an Application.

4.6 Adjustable Regulator Application

The MIC5271YM5 can be adjusted from –1.20V to –14V by using two external resistors (Figure 4-2). The resistors set the output voltage based on Equation 4-4.

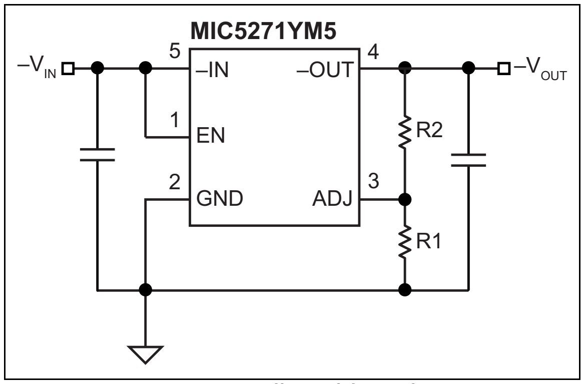


FIGURE 4-2: *Adjustable Voltage Application.*

EQUATION 4-4:

$$|V_{OUT}| = V_{REF}\left(1 + \frac{R2}{R1}\right)$$

Where:

V_{REF} = 1.20V

5.0 PACKAGING INFORMATION

5.1 Package Marking Information

5-Pin SOT23*	Example
XXXX NNN	L930 NNN

Part Number	Output Voltage	Marking
MIC5271YM5	Adjustable	L9AA
MIC5271-3.0YM5	–3.0V	L930
MIC5271-5.0YM5	–5.0V	L950

Legend:

XX...X	Product code or customer-specific information
Y	Year code (last digit of calendar year)
YY	Year code (last 2 digits of calendar year)
WW	Week code (week of January 1 is week '01')
NNN	Alphanumeric traceability code
(e3)	Pb-free JEDEC® designator for Matte Tin (Sn)
*	This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package.

●, ▲, ▼ Pin one index is identified by a dot, delta up, or delta down (triangle mark).

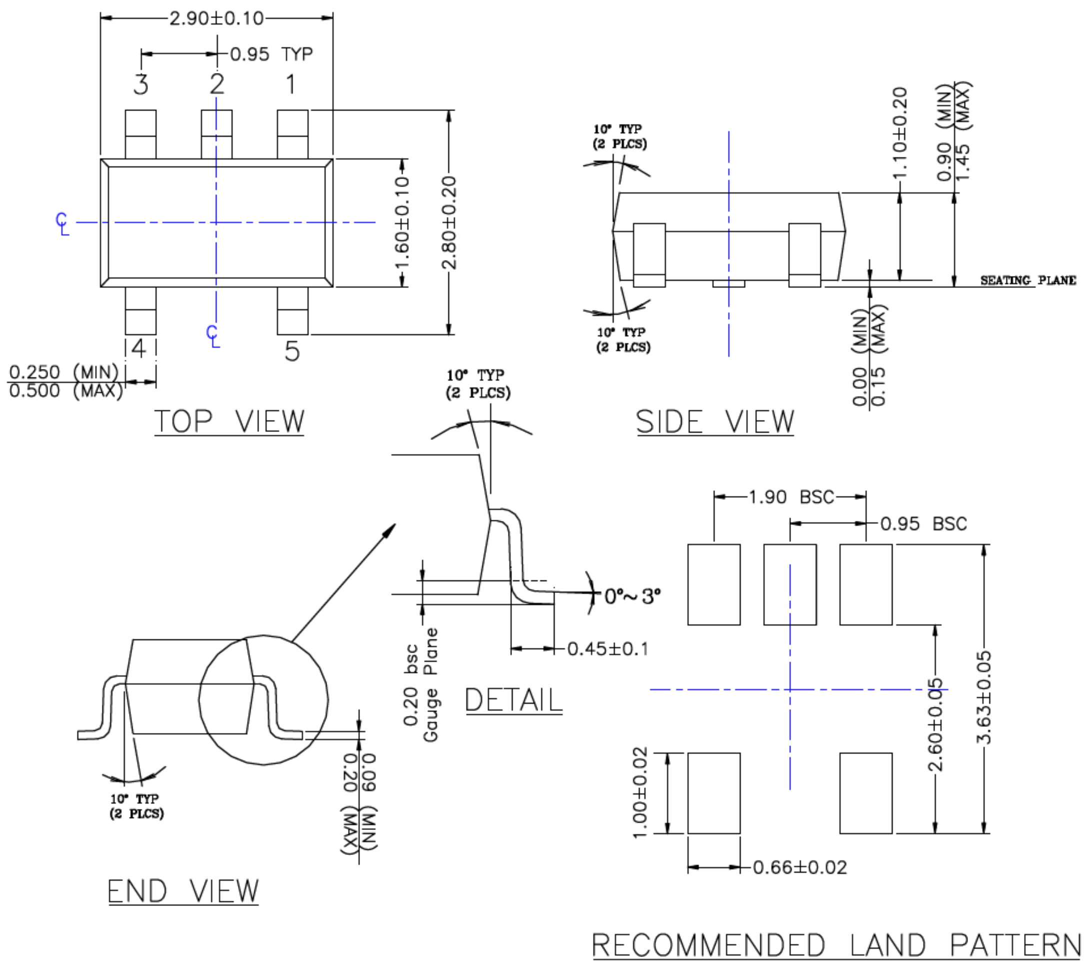
Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.

Underbar (_) and/or Overbar (‾) symbol may not be to scale.

5-Lead SOT23 Package Outline and Recommended Land Pattern

TITLE
5 LEAD SOT23 PACKAGE OUTLINE & RECOMMENDED LAND PATTERN

DRAWING #	SOT23-5LD-PL-1	**UNIT**	MM



NOTE:
1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & BURR.
2. PACKAGE OUTLINE INCLUSIVE OF SOLER PLATING.
3. DIMENSION AND TOLERANCE PER ANSI Y14.5M, 1982.
4. FOOT LENGTH MEASUREMENT BASED ON GAUGE PLANE METHOD.
5. DIE FACES UP FOR MOLD, AND FACES DOWN FOR TRIM/FORM.
6. ALL DIMENSIONS ARE IN MILLIMETERS.

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.

NOTES:

APPENDIX A: REVISION HISTORY

Revision A (November 2017)

- Converted Micrel document MIC5271 to Microchip data sheet DS20005881A.
- Minor text changes throughout.

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

PART NO.	-X.X	X	X	–XX
Device	Output Voltage	Junction Temp. Range	Package	Media Type

Device:	MIC5271:		µCap Negative Low Dropout Regulator
Output Voltage:	<blank>=		Adjustable
	3.0	=	–3.0V Fixed Option
	5.0	=	–5.0V Fixed Option
Junction Temperature Range:	Y	=	–40°C to +125°C, RoHS-Compliant
Package:	M5	=	5-Lead SOT23
Media Type:	TR	=	3,000/Reel

Note: Contact Marketing for other output voltage options.

Examples:

a) MIC5271YM5-TR: µCap Negative Low Dropout Regulator, Adjustable Output Voltage, –40°C to +125°C Temp. Range, 5-Lead SOT23, 3,000/Reel

b) MIC5271-3.0YM5-TR: µCap Negative Low Dropout Regulator, –3.0V Output Voltage, –40°C to +125°C Temp. Range, 5-Lead SOT23, 3,000/Reel

c) MIC5271-5.0YM5-TR: µCap Negative Low Dropout Regulator, –5.0V Output Voltage, –40°C to +125°C Temp. Range, 5-Lead SOT23, 3,000/Reel

Note 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.

NOTES:

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
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QUALITY MANAGEMENT SYSTEM CERTIFIED BY DNV

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